

EMBEDDED SERVER CHASSIS HARDWARE MASTER
SYSTEM AND METHOD

ABSTRACT OF THE DISCLOSURE

A system and method for monitoring a plurality of server processing cards of a server chassis includes, selecting at least one hardware master from among a plurality of server processing cards. A hardware master control signal may be transmitted to the hardware master. A master control module coupled with the hardware master may be activated in response to the master control signal. In accordance with a particular embodiment of the present invention, the hardware master may be operable to at least partially monitor operating information regarding at least a subset of the plurality of server processing cards. The operating information may include health and/or configuration data associated with the subset of the plurality of server processing cards.

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